**Face-To-Face Student/Supervisor Meeting Record**

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| **Project Title:** | Design and Implementation of a Multi-core Processor using FPGA | **Photo:** |  |
| **Student Name:** | Matteo Bovino | **Student ID:** | 8671055 |
| **Supervisor:** | Server Kasap | **Student UID:** |  |
| **Supervisor UID:** |  | **Department:** | AAEEE |
| **Course Code:** | Electrical and Electronic Engineering | **Module Code:** | 305AAE / 306AAE |
| **Date Today:** | 20/11/2020 | **Time:** | 13:00 AM |

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| **Current Progress and Issues:** | |
| *In this meeting we discussed about the structure for the final report, its content and when it should be submitted to the supervisor prior to the deadline. We specified the general guidelines to follow throughout the whole report by analysing the points mentioned in the Moodle page. Every aspect has been clarified including templates and literature review. The report will be written in a fashion similar to the literature review, additions will be made and shared days ahead of the deadline in order to have further clarifications.* | |
| **Agreed Key Action Points:** | |
| *The report is going to be centred around the project proposal and the importance of this device within the vast field of FPGA and computer architecture. Tables, charts and references will be added in order to summarize the main concepts. Changes to the documents developed previously will also be made, this is due to the limited word count available for the whole document. Thanks to the new deadline (14/12/2020) other aspects of the project can be carried forward with no interruptions. Besides the report, I must in fact, finalize the design specifying all the key components. To do so, it’s necessary that I continue gaining information about Systemverilog and FPGAs. In particular, I need to focus on the simulations and exercises present in both the FPGA prototyping book and in the Xilinx workshops, which are great resources for this project.* | |
| **Date and Time of next meeting:** | 13:00 AM 4/12/2020 |

*Signatures of those present:*

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| **Supervisor:** |  |
| **Student: Matteo Bovino** |  |